



SSCN114GN5

Digital Transistor(built-in resistors)

➤ Features

VCC	VIN	IO	R1	R2/R1
50V	-6~+40	70mA	10kΩ	4.7

➤ Description

Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).

The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.

Only the on/off conditions need to be set for operation, making the device design easy.

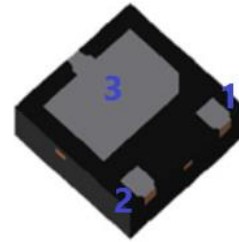
➤ Applications

- Inverter
- Interface
- Driver

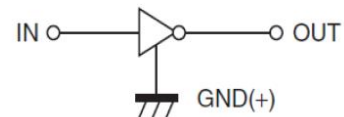
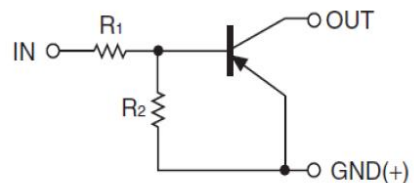
➤ Ordering Information

Device	Package	Shipping
SSCN114GN5	DFN1616-3L	3000/Reel

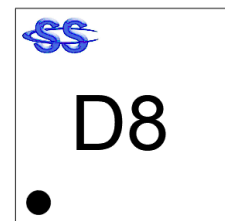
➤ Pin configuration



DFN1616-3L



Circuit Diagram



Marking(Top View)



➤ **Absolute Maximum Ratings**($T_A=25^{\circ}\text{C}$ unless otherwise noted)

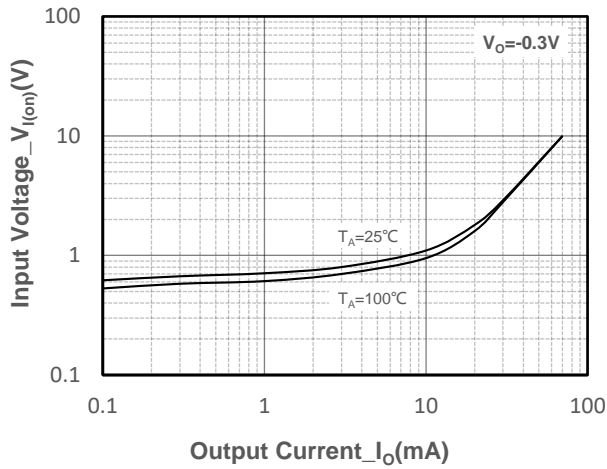
Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	50	V
Input Voltage	V_{IN}	-6~+40	V
Output current	I_o	70	mA
Power Dissipation	P_D	100	mW
Junction Temperature	T_J	-55 to 150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55 to 150	$^{\circ}\text{C}$

➤ **Electrical Characteristics** ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

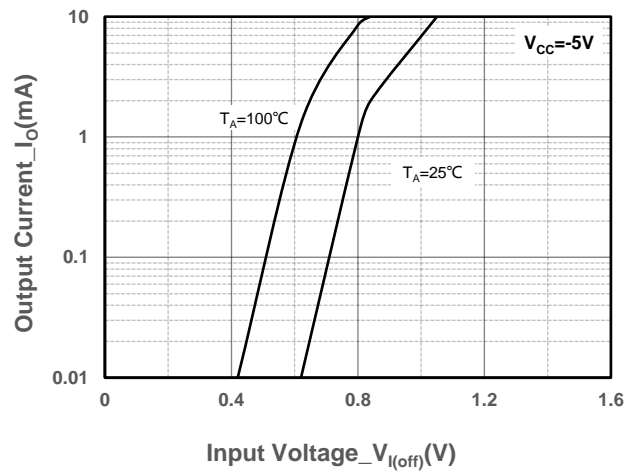
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Input Voltage	$V_{I(off)}$	$V_{CC}= 5V, I_o= 100\mu A$	0.3			V
	$V_{I(on)}$	$V_o= 0.3V, I_o= 5mA$			1.4	V
Output Voltage	$V_{ON(on)}$	$I_o/I_i= 5mA/0.25mA$			0.3	V
Input Current	I_i	$V_i= 5V$			0.88	mA
Output Current	$I_{O(off)}$	$V_{CC}= 50V, V_i=0$			0.5	μA
DC Current Gain	G_1	$I_c= 5V, I_o= 10mA$	68			
Input resistance	R_1		7	10	13	k Ω
Resistance ratio	R_2/R_1		3.7	4.7	5.7	
Transition frequency	f_T	$V_o= 10V, I_o= 5mA$ $f= 100MHz$		250		MHz



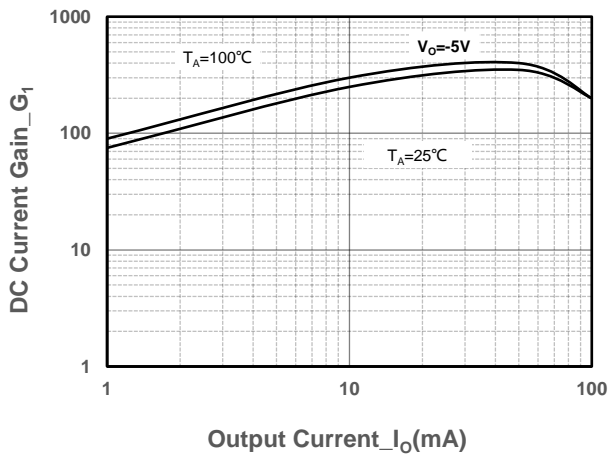
➤ **Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)**



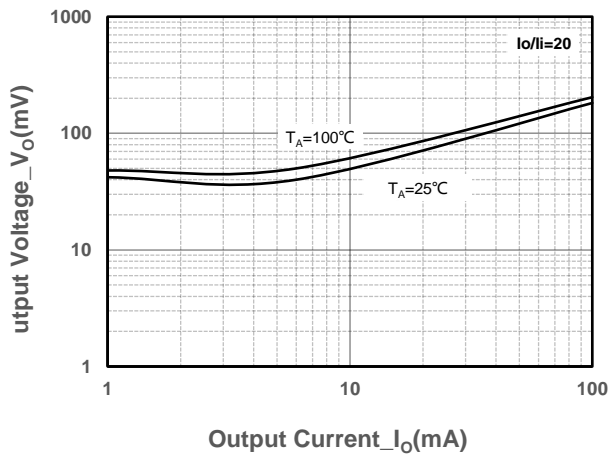
Input Voltage vs. Output Current



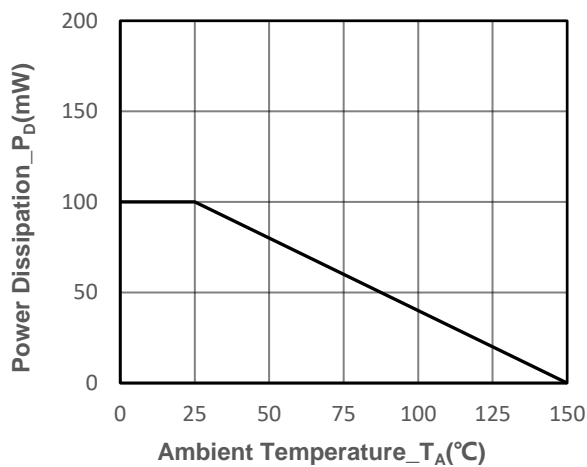
Output Current vs. Input Voltage



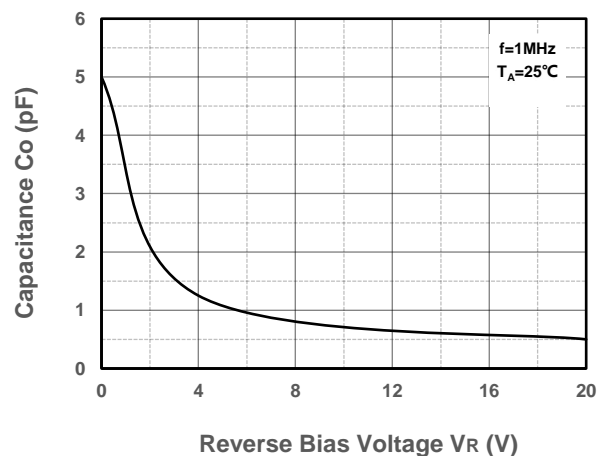
DC Current Gain vs. Output Current



Output Voltage vs. Output Current

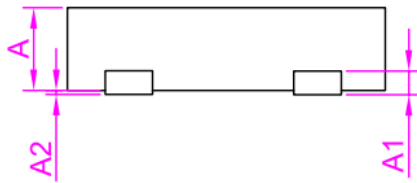
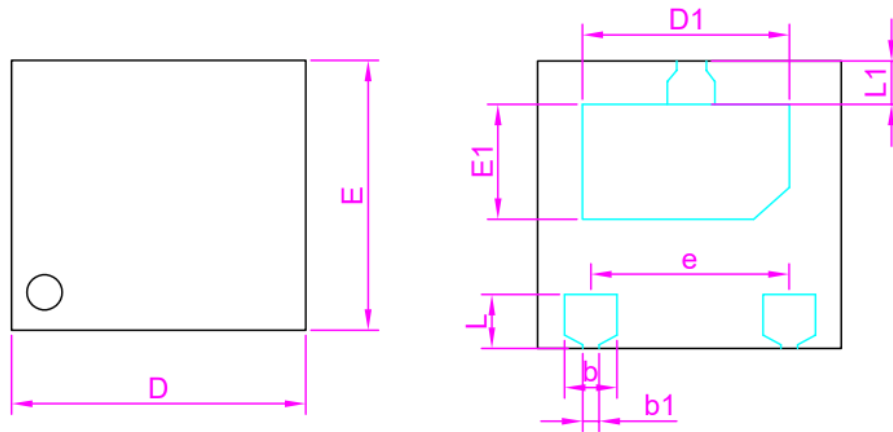


Power derating vs. Ambient temperature



Capacitance vs. Reverse Bias Voltage

- Package Information



DIM	Millimeters		
	Min.	Typ.	Max.
A	0.50	0.55	0.60
D	1.55	1.60	1.65
E	1.55	1.60	1.65
b	0.35	0.40	0.45
L	0.35	0.40	0.45
e	1.00BSC		
D1	1.15	1.20	1.25
E1	0.50	0.55	0.65
b1	0.15	0.20	0.25
L1	0.20	0.25	0.30
A1	0.15BSC		
A2	0.00	0.025	0.05



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